



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the Application of

Attn: OIPE

Kazuhiro MASUDA

Application No.: 10/799,621

Docket No.: 119108

Filed: March 15, 2004

For: SEMICONDUCTOR DEVICE, METHOD OF MANUFACTURING THREE-DIMENSIONAL STACKING TYPE SEMICONDUCTOR DEVICE, CIRCUIT BOARD, AND ELECTRONIC INSTRUMENT


REQUEST FOR CORRECTION OF PALM RECORDS

Commissioner for Patents
P.O. Box 1450
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Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records. A Supplemental Application Data Sheet is attached.

Respectfully submitted,


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Date: June 23, 2004

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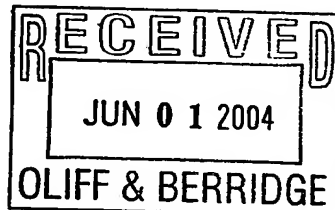
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/799,621	03/15/2004	2811	770	119108	9	4	1

25944
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CONFIRMATION NO. 8397

FILING RECEIPT



OC000000012808793

Date Mailed: 05/28/2004

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Applicant(s)

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Assignment For Published Patent Application

SEIKO EPSON CORPORATION, Tokyo, JAPAN;

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2003-088825 03/27/2003

JAPAN 2003-424713 12/22/2003

If Required, Foreign Filing License Granted: 05/27/2004

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor device, method of manufacturing three-dimensional stacking type semiconductor device, circuit board, and electronic instrument

Preliminary Class

257

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